NEW: Read important application notes on page 4 ff.





Features

- Schottky-type photodiode
- Intrinsic visible blindness due to wide-bandgap semiconductor material
- · Large photoactive area
- No focusing lens needed, therefore large usable incident angle
- Designed to operate in photovoltaic mode
- TO-39 metal package

Maximum Ratings

Parameter	Symbol	Value	Unit
Operating temperature range	T_{opt}	-20 +80	°C
Reverse voltage	V_{Rmax}	3	V
Forward current	I _{Fmax}	5	mA
Total power dissipation at 25°C	P _{tot}	5	mW

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General Characteristics

 $(T_a = 25 \, ^{\circ}C)$

Parameter	Symbol	Value	Unit
Active area	А	15,66	mm^2
Active area dimensions	LxW	5.4 x 2.9	mm^2
Max. viewing angle	α	арр. 80	degree
Shunt resistance (dark)	R_s	100	$M\Omega$
Dark current at 10mV reverse bias	I _d	100	рА
Open circuit voltage (200μW/cm², λ=300nm)	V_0	>200	mV
Short circuit current (200μW/cm², λ=300nm)	I ₀	650	nA
Breakdown voltage (dark)	V_{BR}	> 3	V

Spectral Characteristics

 $(T_a = 25 \, ^{\circ}C)$

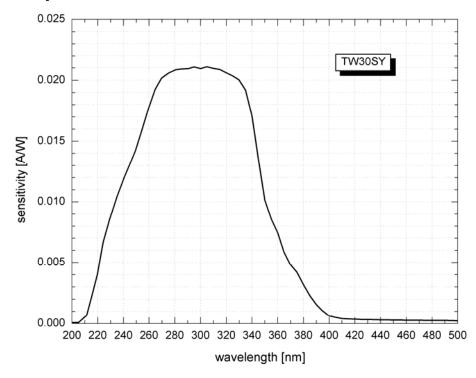
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Parameter	Symbol	typ. Value	Unit
Max. spectral sensitivity	S_{max}	21	mA W ⁻¹
Wavelength of max. spectral sensitivity	λsmax	300	nm
Range of spectral sensitivity (S=0.1*S _{max})	-	215 – 387	nm
Visible blindness	$\frac{S_{max}}{S_{400nm}}$	50	

Spectral Response

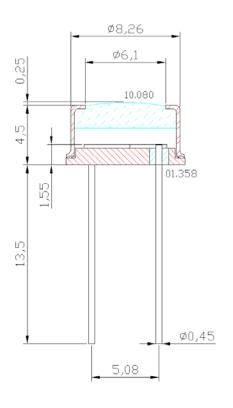


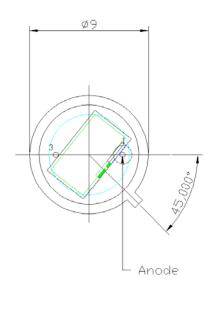
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Pin Layout





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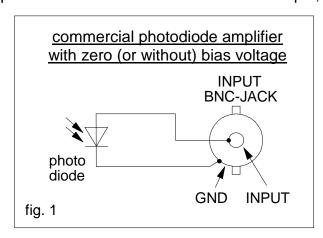


Application notes

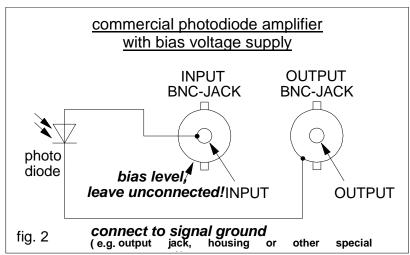
Our polycrystalline UV photodiodes are designed for **photovoltaic operation**. This operation mode is necessary to minimize the dark current of large area photo detectors which otherwise needs to be considered in the commonly used photoconductive mode.

First we want to show the implementation of photovoltaic operation with **commercially available** photodiode (current) **amplifiers**.

Many amplifier devices provide an adjustable bias voltage. This has to be switched off or trimmed to well below 0.1 V in order to ensure photovoltaic operation. In this case the connection of our photodiodes to such devices is rather simple, see figure 1.



Some amplifiers only have a fixed bias voltage or the bias cannot be trimmed to near zero. In this case it is required to draw the ground level (which is not available on the input jack) from other sources, e.g. the output jack, special connectors or even from the housing. Please refer to the amplifier's manual and figure 2.



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Design of custom photodiode amplifiers

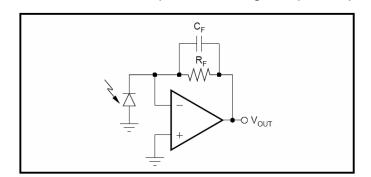
This complex topic strongly depends on your specific application. We provide flexible "ready to use" amplifier boards, consultation, development support as well as engineering solutions.

Nevertheless, we offer some examples, references for further reading and keywords for your convenience.

Use amplifier chips with low input offset voltages and currents. Examples:

high end: OPA128 (Texas instruments, Burr Brown) medium: TLV277x, TLC227x (Texas Instruments) low end: TL07x, TL08x (Texas instruments)

Use transimpedance setup with feedback resistors not above 10 MΩ and without bias voltage. The figure below shows the basic schematic, however, all textbooks on basics of electronics cover plenty of details. Please also refer to the application note "DESIGNING PHOTODIODE AMPLIFIER CIRCUITS WITH OPA128", (Texas Instruments, Burr Brown). If amplification above 10⁷ V/A is required two stage amplifiers perform better.



- For stability reasons apply a feedback capacitor parallel C_F to the feedback resistor R_F. Value depends on various parameters.
 Please also refer to the application note "COMPENSATE TRANSIMPEDANCE AMPLIFIERS INTUITIVELY", (Texas Instruments, Burr Brown).
 Hint: Use 10 nF with OPA128 and 2 nF with TL07x, higher values minimize the noise dependence but also the bandwidth.
- Always use proper shielding, even on PCB: guard layers and wires are strongly recommended; reduce distances between sensor and amplifier chip; prevent ground loops.
- For general understanding of operational amplifier properties refer to the white paper (SLOA011) "Understanding Operational Amplifier Specifications" from Texas Instruments.

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For further information, please feel free to contact us. All references can be found on the Texas Instruments website www.ti.com. Given products and information are to be considered as examples only. No guarantee is given for completeness or correctness. We take no responsibility for damages caused by using this information.

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